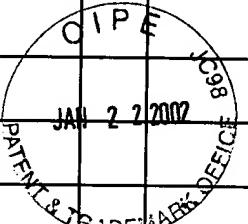
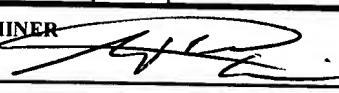


INFORMATION DISCLOSURE STATEMENT <i>(Use several sheets if necessary)</i>			Docket Number (Optional) OKI.147		Application Number 09/497,499		
			Applicant(s) K. Asakawa, et al.				
			Filing Date 02/04/2000		Group Art Unit 2814		
U.S. PATENT DOCUMENTS							
*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
							
FOREIGN PATENT DOCUMENTS							
REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
YES	NO						
OTHER DOCUMENTS <i>(Including Author, Title, Date, Pertinent Pages, Etc.)</i>							
WSL	1.	Sorab K. Ghandhi; "VLSI Fabrication Principles, Silicon and Gallium Arsenide" (Second Edition); Copyright 1994; pg. 528 and pg. 725; A Wiley-Interscience Publication, John Wiley & Sons, Inc.					
EXAMINER 			DATE CONSIDERED 4/20/02				

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.